

## **APPLICATION DATA SHEET**

### **APPLICATION INFORMATION**

Filing Date:: 07-11-03  
Application Type:: Regular  
Subject Matter:: Utility  
Title:: Counter-Based Clock Doubler  
Circuits and Methods with Optional  
Duty Cycle Correction and Offset  
Attorney Docket Number:: X-1464 US  
Request for Early Pub?:: No  
Request for Non-Pub?:: Yes  
Total Drawing Sheets:: 12  
Small Entity?:: No  
Petition included?:: No

### **APPLICANT INFORMATION**

Applicant Authority Type:: Inventor  
Primary Citizenship Ctry:: US  
Status:: Full Capacity  
Given Name:: Andy  
Middle Name:: T.  
Family Name:: Nguyen  
Street:: 775 Salt Lake Drive  
City:: San Jose  
State or Province:: California  
Postal or Zip Code:: 95133

### **CORRESPONDENCE INFORMATION**

Correspondence Customer Number:: 24309

**REPRESENTATIVE INFORMATION**

Representative Customer Number::	24309	
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**ASSIGNEE INFORMATION**

Assignee Name:: Xilinx, Inc.  
Street:: 2100 Logic Drive  
City:: San Jose  
State or Province:: California  
Postal or Zip Code:: 95124